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ABSTRACT OF THE DISCLOSURE

A quad type liquid crystal display device includes a liquid crystal panel having gate and data lines which define sub-pixel regions. Gate driving integrated circuits for driving the gate lines are provided. A plurality of data drive integrated circuits are arranged on one side of the liquid crystal panel. Each of the data drive integrated circuits have "m" (m is a natural number) number of channels, wherein $(3n-1)$ th (n is a natural number) channels for each data drive integrated circuit are floating.
